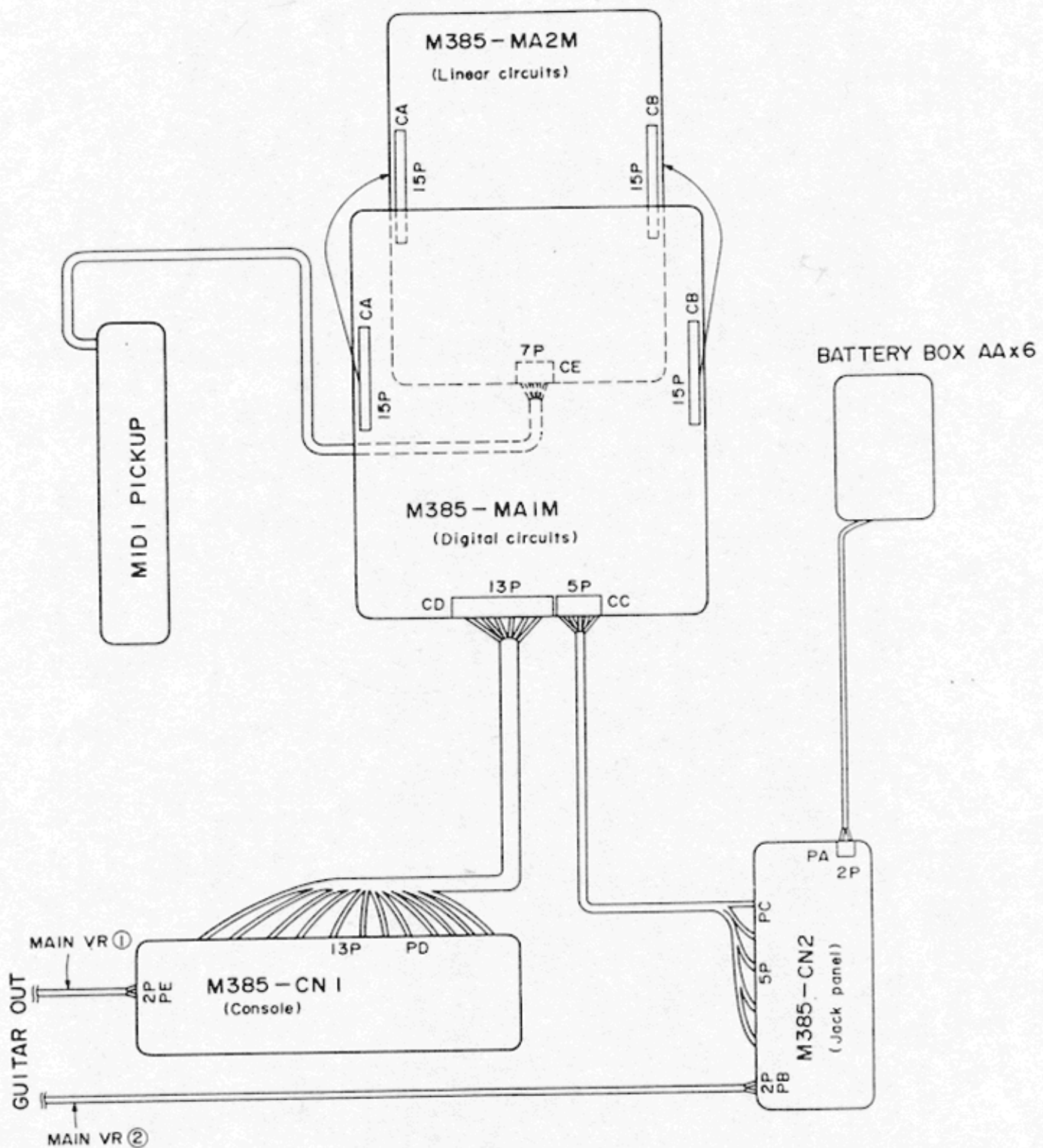
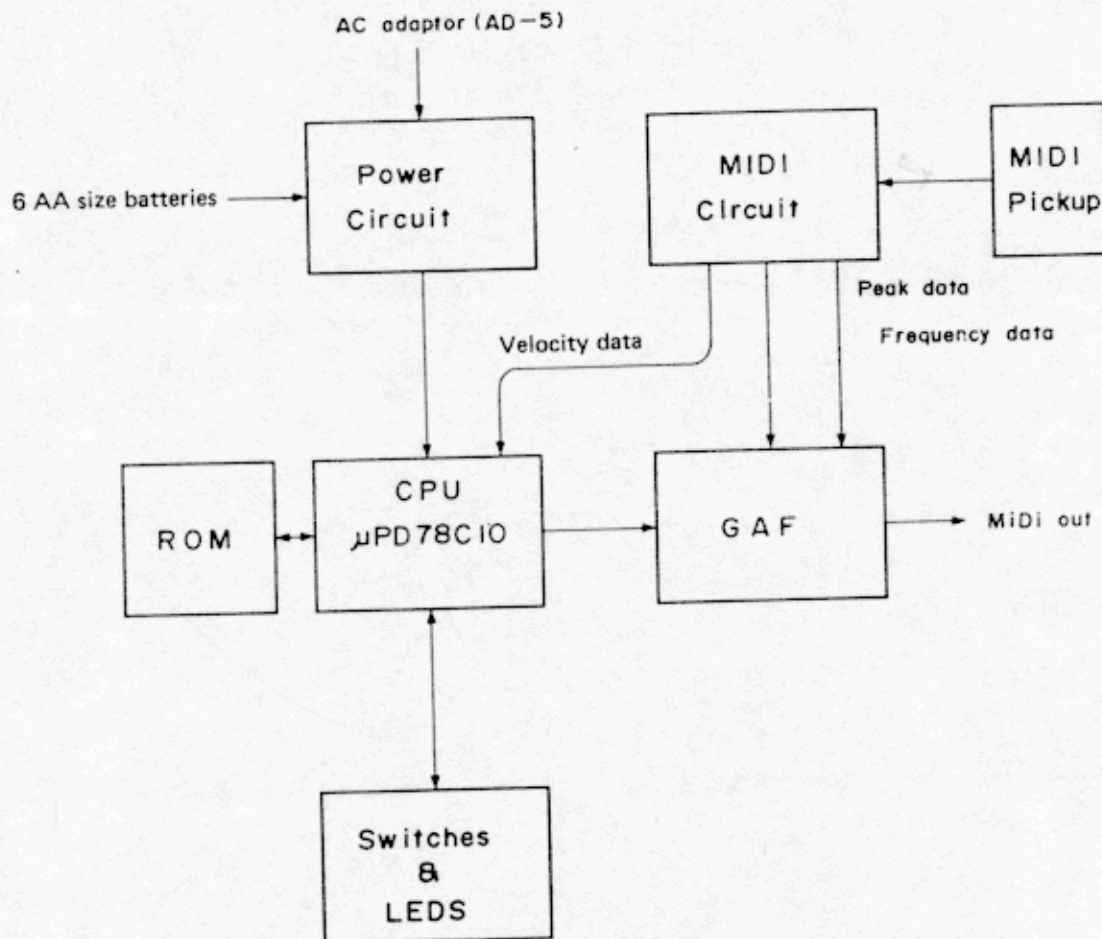


### 3 CIRCUIT DESCRIPTION

#### 3-1 WIRING DIAGRAM



### 3-2 BLOCK DIAGRAM



Linear Circuit separates the signal from MIDI pickup into three kinds of data which are velocity, peak, and frequency data.

Gate Array F contains sexadecimal counters which counts up frequency data and send it to CPU as a pitch data. Peak signal is used for the timing signal of the counters.

Velocity data is transformed into digital data in built-in ADCs in CPU.

The pitch and the velocity data are converted into MIDI signal in CPU and sent to MIDI OUT terminal via a buffer in Gate Array F.

### 3-3 CIRCUIT BLOCK

